

## ABSTRACT OF THE DISCLOSURE

The present invention relates to a processing apparatus comprised with an internal circuit having a CPU and internal devices and an external circuit including external devices provided externally of the internal circuit, and the like, and is aimed to prevent illicit access and reverse engineering.

The present invention comprises an internal circuit 100 including a CPU 101, internal devices 102 to 105 and a bus line 110 connecting the CPU 101 to the internal devices 102 to 105 and extending externally, and an external circuit 200 including external devices 201 to 203 and 211 to 212 provided externally of an externally extending portion 110a of the bus line 110. The internal circuit 100 includes a ciphering section 120 provided at an entrance to an external side and ciphering addresses and data on the bus line 110 by ciphering patterns according to a plurality of regions divided from an address space allotted to the entire external devices.